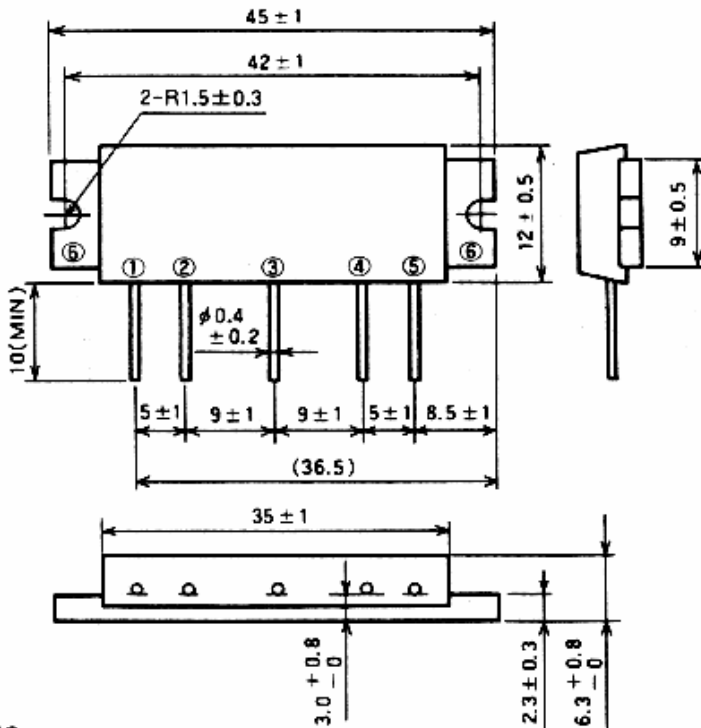


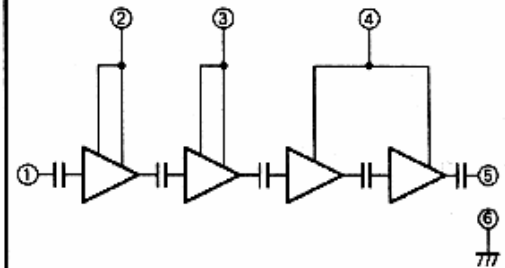
**OUTLINE DRAWING**

Dimensions in mm



H13

**BLOCK DIAGRAM**



PIN :

- ① Pin : RF INPUT
- ② Vcc1 : 1st. DC SUPPLY
- ③ Vcc2 : 2nd. DC SUPPLY
- ④ Vcc3 : 3rd. DC SUPPLY
- ⑤ Po : RF OUTPUT
- ⑥ GND : FIN

**ABSOLUTE MAXIMUM RATINGS** (T<sub>c</sub> = 25 °C unless otherwise noted)

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>cc1, 2</sub>	Supply voltage	P <sub>o</sub> ≤ 3W, V <sub>cc3</sub> ≤ 9V	9	V
V <sub>cc3</sub>		P <sub>o</sub> ≤ 3W, V <sub>cc1, 2</sub> ≤ 7.2V	16	V
I <sub>cc</sub>	Total current		1.5	A
P <sub>in(max)</sub>	Input power	Z <sub>G</sub> = Z <sub>L</sub> = 50Ω, P <sub>o</sub> ≤ 3W, V <sub>cc1, 2</sub> ≤ 7.2V	10	mW
P <sub>o(max)</sub>	Output power	Z <sub>G</sub> = Z <sub>L</sub> = 50Ω, V <sub>cc1, 2</sub> ≤ 7.2V	3	W
T <sub>c(OP)</sub>	Operation case temperature	Z <sub>G</sub> = Z <sub>L</sub> = 50Ω, V <sub>cc1, 2</sub> ≤ 7.2V	- 20 to 100	°C
T <sub>stg</sub>	Storage temperature	Z <sub>G</sub> = Z <sub>L</sub> = 50Ω, V <sub>cc1, 2</sub> ≤ 7.2V	- 40 to 110	°C

Note. Above parameters are guaranteed independently.

**ELECTRICAL CHARACTERISTICS** (T<sub>c</sub> = 25 °C unless otherwise noted)

Symbol	Parameter	Test conditions	Limits		Unit
			Min	Max	
f	Frequency range	V <sub>cc1</sub> = V <sub>cc2</sub> = V <sub>cc3</sub> = 7.2V P <sub>in</sub> = 7mW Z <sub>G</sub> = Z <sub>L</sub> = 50Ω	1.24	1.3	GHz
P <sub>o</sub>	Output power		1.5		W
η <sub>T</sub>	Total efficiency		28		%
2f <sub>o</sub>	2nd. harmonic			- 30	dBc
ρ <sub>in</sub>	Input VSWR			3.5	-
-	Load VSWR tolerance	V <sub>cc2</sub> = 9V, V <sub>cc3</sub> = 15V; P <sub>in</sub> = 7mW P <sub>o</sub> = 2W (V <sub>cc1</sub> : controlled) Load VSWR = 10 : 1 (All phase), Z <sub>G</sub> = 50Ω	No degradation or destroy		-

Note. Above parameters, ratings, limits and conditions are subject to change.